

**D 71062**

**(Pages 2)**

**Name**

**Reg. No. ....**

**FIFTH SEMESTER B.C.A DEGREE EXAMINATION, NOVEMBER 2014**

**(UG-CCSS)**

**Core Course**

**CA 5B 08—MICRO PROCESSOR**

Time : Three Hours

Maximum : 30 **Weightage**

**I. Answer all *twelve* questions :**

- 1 8086 has \_\_\_\_\_ **datalines.**
- 2 Stack point register contains \_\_\_\_\_
- 3 Zero flag is set when \_\_\_\_\_
- 4 The way in which an operand is specified is called its \_\_\_\_\_
- 5 \_\_\_\_\_ is an example of data transfer instruction.
- 6 A **16-bit** microprocessor has the word length equal to \_\_\_\_\_
- 7 \_\_\_\_\_ processor has a super scalar architecture.
- 8 8259 is \_\_\_\_\_
- 9 \_\_\_\_\_ special segment of program that can be called for execution from any point in a program.
- 10 A set of conductors used for communicating information between the components in a computer system is called \_\_\_\_\_
- 11 **Maskable** interrupts use the \_\_\_\_\_ signal line.
- 12 The process of taking data from stack is called \_\_\_\_\_

(12 x = 3 **weightage**)

**II. Answer all *nine* questions :**

- 13 Define functions of flag register.
- 14 What is meant by immediate address mode ?
- 15 Explain subroutine.
- 16 Write any 4 logical instructions.
- 17 What are the different functional units in 8086 ?
- 18 Give structure of MACRO definition.

**Turn over**

- 19 Explain branch instructions in 8086.  
 20 Why 8086 had 1MB memory ?  
 21 Explain Target machine code Generation Control Directives.

(9 x 1 = 9 weightage)

**III.** Answer any *five* questions :

- 22 Explain different data movement instructions in 8086.  
 23 Explain different addressing modes in 8086.  
 24 Write a note on target machine code generation.  
 25 Explain concept of Modular Programming.  
 26 What is DMA?  
 27 Explain Concept of pipelining.  
 28 Write the applications of 8259 and 8255.

(5 x 2 = 10 weightage)

**IV.** Answer any *two* questions :

- 29 Explain internal processor architecture of 8086 using functional block diagram.  
 30 Discuss Interrupts and interrupt routine in detail.  
 31 Compare features of 8086,486 and Pentium.

(2 x 4 = 8 weightage)